

Capacitor Voltage Balancing in Modular Multilevel Converter

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ABSTRACT

Capacitor voltage balancing is a critical issue for neutral-point-clamped-based converters, including the two/three-level dual-active-bridge dc-dc converters. The unbalanced capacitor voltage increases the voltage stress on power devices and negatively affect the reliability of the converters. The electrical energy is obviously the dominant from among others due to its flexibility of conversion to other forms, ease of transmission to the consumption site, low environment effect and its sustainable character. It is obvious that the demand of electrical energy increases tremendously, not only because of the population increase, but also the faster increasing automation in every aspect of life. Current world needs reliable supply of the electrical energy with high quality, meeting the national and international relevant standards. High efficiency, low cost and infrequent maintenance are needed as well as environmental friendliness are being expected from the electrical energy system of today. This paper focuses on different PWM control method for capacitor voltage balancing MMC. The performance of voltage balancing is obtained better than existing methods. The proposed topologies have been simulated in MATLAB/Simulink environment and performances were compared with existing methods.

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1. INTRODUCTION

In the 20th century, electricity has become the dominant form of energy due to its ease of conversion to other forms. Today, the modern civilization based its operation on an increasing energy demand, substituting human activities with complex and sophisticated machines mainly run by electric power. Indeed, the world energy consumption is expected to increase by more than 54% every ten years [1]. Electricity will be more dominant than ever while supplying this energy demand since the countries try to bypass especially fossil fuels in the supply chain of energy. In order to meet the increasing demand, electric power generation, transmission, distribution and conversion become more and more important.

The recent attention in environmental protection and preservation has lead countries to shift to sustainable and renewable energy sources. Electric power generation from solar and wind energy is wide spread today. These resources will diffuse and occupy an increasingly important role in energy production in coming years. Therefore, the need of high quality electric power conversion systems to be used in this area is greater than ever. From the beginning of the 21st century, many countries have chosen to deregulate their electricity market. The situation resulted in a mix of energy

sources while pursuing higher efficiency, particularly with the introduction of private investments in the energy market.

In the light of above mentioned events, in order to accomplish all requirements both from users and legal regulations, and to reduce the environmental impact, power conversion and control is needed to be reliable, safe, efficient, and available.

Voltage Source Converter (VSC) technology has been becoming common in many of modern demanding electric power systems, such as high-voltage direct current (HVDC) transmission.

Synthesis of output voltage waveform in conventional two- or three-level VSCs is done by reflecting half of the dc-link voltage to the output, which results in high harmonic content and requirement for output harmonic filters which increase both the cost and footprint of the system. On the other hand, that of the MMC topology is based on the sum of small voltage steps reducing harmonic content, and removing the need of harmonic filter at the output.

A. Typical Application Areas of MMC

HVDC transmission is an important and efficient alternative to three-phase ac transmission of electric power over long distances. Advantages for choosing HVDC transmission instead of three-phase ac transmission can be numerous and should be considered in individual situation apart. However, the mostly acknowledged advantages could be summarized as follows: lower transmission losses, the capacity to transfer more power over the same right of way, the ability to interconnect systems that are not synchronized or using different frequencies, short-circuit currents limitation, long distance water crossing capability. One of the most important advantages of HVDC transmission on ac transmission is the ability of controlling active power transmission accurately, while ac lines power flow cannot be controlled in the same direct way. Moreover, in [3] it is shown that the cost of HVDC transmission is less than ac transmission above a distance of 800km overhead line or 50km underground or submarine cables. This is due to the fact that the higher cost of HVDC converters is overcome by the cost of reduced number of transmission line conductors. Figure 1.3 shows the cost.

Systems (especially in offshore wind power transmission), medium-voltage motor drives, STATCOM applications, electric traction/propulsion systems or grid connected energy storage systems. Today, the majority of the applications of these electric power systems are driven by conventional two- or three-level VSC's. However, these conventional VSC's are not able to respond properly to emerging requirements such as high power, high efficiency, reliability of the system, and environmental compatibility. The solution to these problems has been evolved through the use of combinable, standardized, distributed, and simpler converter structures with lower voltage steps. Indeed, standardized and distributed systems have become the recommended solution to achieve modern projects requirements in all engineering areas. These configurations provide a more reliable operation, facilitates fault diagnosis, maintenance, and reconfigurations of control system. Especially in fail safe situations, distributed configuration allows control system to isolate the problem, drive the process in safe state easily, and in many cases allows continuing almost normal operation in faulty conditions. Multilevel converters have been progressed as significant competitors to the conventional VSCs in order to overcome the abovementioned limitations. At present, modular multilevel converter (MMC, MMLC, M2LC, M2C) (Figure 1.1) is at the heart of research and development studies in power electronics area, both for academic and industrial scope. Its unique chopper cell based topological structure was firstly proposed by Lesnicar and Marquardt in 2003 [2]. The initial target was very high-voltage applications, especially network inerties in power generation and transmission. Since then, the topology had a great attraction due to its promising features. Compared to conventional VSC technology, MMC offers advantages such as higher voltage and power levels, modular and redundant construction, and longer maintenance intervals, of controlling active power transmission accurately, while ac lines power flow cannot be controlled in the same direct way. Moreover, in [3] it is shown that the cost of HVDC transmission is less than ac transmission above a distance of 800km overhead line or 50km underground or submarine cables. This is due to the fact that the higher cost of HVDC converters is overcome by the cost of reduced number of transmission line conductors. Figure 2 shows the cost estimations for ac and HVDC transmission by distance via overhead transmission line.

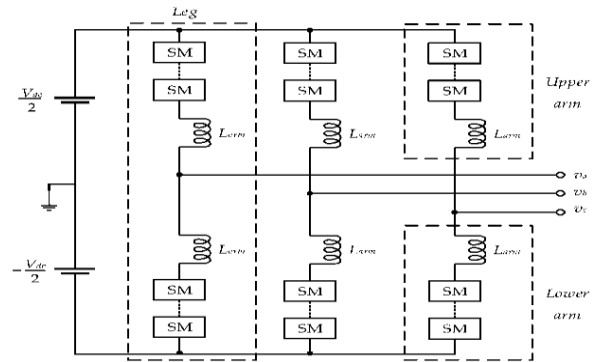


Figure 1: Modular Multilevel Converter

Power systems are driven by conventional two- or three-level VSC's. However, these conventional VSC's are not able to respond properly to emerging requirements such as high power, high efficiency, reliability of the system, and environmental compatibility. The solution to these problems has been evolved through the use of combinable, standardized, distributed, and simpler converter structures with lower voltage steps. Indeed, standardized and distributed systems have become the recommended solution to achieve modern projects requirements in all engineering areas. These configurations provide a more reliable operation, facilitates fault diagnosis, maintenance, and reconfigurations of control system. Especially in fail safe situations, distributed configuration allows control system to isolate the problem, drive the process in safe state easily, and in many cases allows continuing almost normal operation in faulty conditions. Multilevel converters have been progressed as significant competitors to the conventional VSCs in order to overcome the abovementioned limitations. At present, modular multilevel converter (MMC, MMLC, M2LC, M2C) (Figure 1.1) is at the heart of research and development studies in power electronics area, both for academic and industrial scope. Its unique chopper cell based topological structure was firstly proposed by Lesnicar and Marquardt in 2003 [2]. The initial target was very high-voltage applications, especially network inerties in power generation and transmission. Since then, the topology had a great attraction due to its promising features. Compared to conventional VSC technology, MMC offers advantages such as higher voltage and power levels, modular and redundant construction, and longer maintenance intervals, of controlling active power transmission accurately, while ac lines power flow cannot be controlled in the same direct way. Moreover, in [3] it is shown that the cost of HVDC transmission is less than ac transmission above a distance of 800km overhead line or 50km underground or submarine cables. This is due to the fact that the higher cost of HVDC converters is overcome by the cost of reduced number of transmission line conductors. Figure 2 shows the cost estimations for ac and HVDC transmission by distance via overhead transmission line.

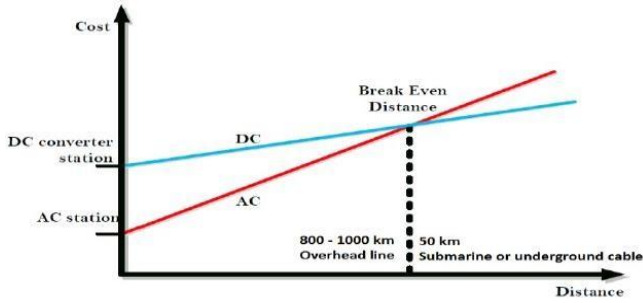


Figure 2: Cost Estimation of AC and HVDC Transmission by distance



Figure 3: HelWin2 offshore HVDC platform of Siemens



Figure 4: Sub-module (up) and MMC (down) structure of MaxSine



Figure 5: MMC based Motor Drive for a converter fed synchronous machine by ABB and Oberhasli Hydroelectric Power Company, Switzerland

B. Reasons for choosing HVDC Transmission System

In an AC system, voltage conversion is simple. An AC transformer allows high power levels and high insulation levels within one unit and has low losses. It is a relatively simple device, which requires little maintenance. Further, a three-phase synchronous generator is superior to a DC generator in every respect. For these reasons, AC technology was introduced at a very early stage in the development of electrical power systems. It was soon accepted as the only feasible technology for generation, transmission and distribution of electrical energy. However, high-voltage AC transmission links have disadvantages, which may compel a change to DC technology.

- i. Inductive and capacitive elements of overhead lines and cables put limits to the transmission capacity and the transmission distance of AC transmission links. For AC cable the range is about 40 to 100 km.
- ii. Direct connection between two AC systems with different frequencies is not possible. So researchers are now doing research on DC transmission instead of AC transmission.

2. MATERIALS AND METHODS

In the field of high voltage level applications, modular multi-level converter (MMC) has the definite advantages of low power loss and modularity and there have been many studies on its reliability. Some researches focus on the degradation of physical characteristics in the lifetime prediction of key devices, but the degradation of physical characteristics has not been directly used in the research of MMC system level reliability. The traditional exponential distribution failure rate is constant while the Monte Carlo method assumes the random distribution of multiple devices. Neither of these two methods can describe the reliability of a single device with physical characteristics degradation. MMC reliability analysis and design method based on MMC mission profile and insulated-gate bipolar transistor (IGBT) lifetime degradation. According to the IGBT current and power loss in MMC, the annual mission profile and junction temperature result are analyzed by rain-flow counting algorithm. In terms of device degradation, the thermal network updating method is used to calculate the life of IGBT in different time, and the reliability analysis method based on exponential distribution is improved. To optimize the redundancy design of the system, the multi-objective function optimization is processed.

Table 1

Several STATCOM projects based on MMC concept

Project	Installed year	Manufacturer	System voltage (ac)(kV)	Power (MVA _r)	Location
Kikiwa	2009	Siemens	220	2x(±50)	New Zealand
The Thanet offshore wind farm	2009	Siemens	11	N/A	UK
Mocuba	2010	Siemens	33	N/A	Mozambique
Cerro Navia	2011	ABB	220	+65/-140	Chile
The Greater Gabbard offshore wind farm	2011	Siemens	13.9	N/A	UK

STATCOM applications are one of the fields for MMC to penetrate in. In order to improve dynamic stability and power quality of power systems which have been deregulated and liberalized especially from the beginning of the 21st century, STATCOM applications are widely used. The MMC topology provides improved dynamic stability of transmission systems, increased power quality, flexibility to adapt to different power ratings, compact design with low footprint as well as redundancy and minimized engineering costs to STATCOM systems. Different manufacturers have MMC based STATCOM systems, some of which are SVC Plus (full-bridge based, Siemens) and SVC Light (full-bridge based, ABB). In Table 1, several STATCOM projects based on MMC topology

3. RESULTS AND DISCUSSION

To verify the effectiveness of the modulation technique, all the capacitor voltage are assumed to be constant. A three phase five level MMC circuit with conventional MMC model with four SMs in each arm is built in MATLAB simulation. According to the waveforms, it is cleared that the capacitors voltages are so much closer to each other. At the beginning of the simulation process, the frequency of reference signal is measured. The frequencies of carrier signals are measured for determining the time period of those carrier signals. Then compared each carrier signal with the reference signal at a certain time period. If the condition satisfied the next carrier signal will be compared with the reference signal otherwise the reference signal will be compared with current carrier signal.

To verify the propose capacitor voltage balancing method and the system performance, a three phase five level conventional MMC circuit is built.

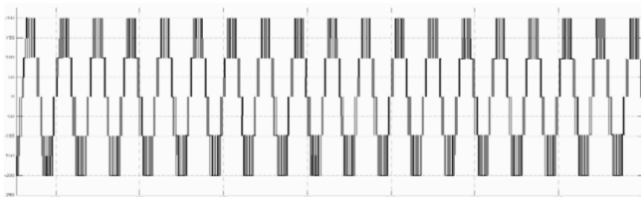


Figure 6: Voltage vs Time curve showing output phase voltage waveform of the 5 level MMC

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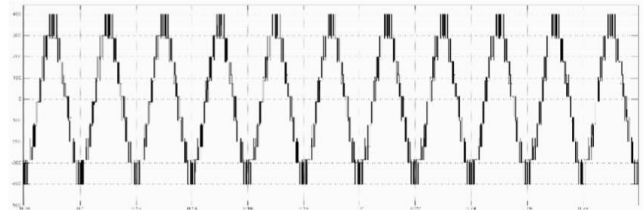


Figure 7: Voltage vs Time curve showing output line voltage waveform of the 5 level MMC

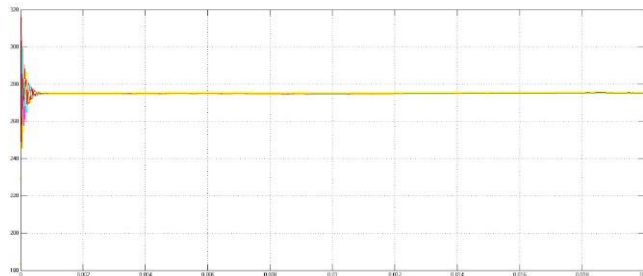


Figure 8: Voltage vs Time curve showing output line voltage graph of the 5 level MMC

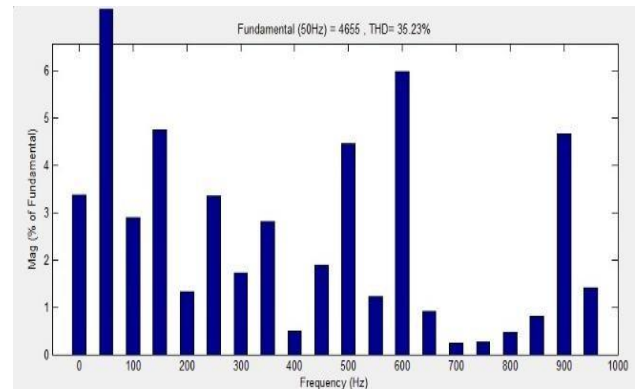


Figure 9: THD in proposed method

The graph of Total Harmonic Distortion (THD) versus frequency typically shows how the THD levels change across different frequencies. In an ideal scenario, THD is expected to be minimal at the fundamental frequency and increase at higher harmonic frequencies. The graph may exhibit a characteristic shape, often resembling a curve or a series of peaks at harmonic frequencies. The goal is to keep THD as low as possible, especially at the fundamental frequency, to ensure efficient and clean operation of the system. Researchers and engineers analyse this graph to assess the performance of devices or systems in terms of harmonic distortion across the frequency spectrum.

Table 2

Summary of the improvement of voltage across each capacitor in each Sub-module which ensure that the amount of circulating current between capacitors is reduced

Sub-Module (SM)	Voltage across each capacitor in volt	
	PD method	Proposed method
SM1	2764	2749
SM2	2735	2746
SM3	2729	2746
SM4	2761	2748

3. CONCLUSION

Energy is the most valuable and vital asset of countries. The energy demand of mankind is increasing day by day. As the machines are replacing manpower since the industrial revolution, modern civilizations have based their survival on energy, anymore. The electrical energy is obviously the dominant form among others due to its flexibility of conversion to other forms, ease of transmission to the consumption site, low environmental effect and its sustainable character. It is obvious that the demand of electrical energy will increase tremendously, not only because of the population increase, but also the faster increasing automation in every aspect of life. Moreover, apart from supplying this increasing demand, renovation of conventional electrical energy sources to sustainable and environmental friendly ones, brings extra workload to electrical power engineering industry. Thus, the electrical energy systems are required to respond properly to this continuously increasing demand [12]. Moreover, they need to be reliable and supply the electrical energy with high quality, meeting the national and international relevant standards. High efficiency, low cost and infrequent maintenance need as well as environmental friendliness are being expected from the electrical energy systems of today.

The conventional two/three-level voltage source converters are not able to respond all these requirements accurately. They have limitations in terms of power capacity, energy quality, semiconductor ratings, reliability, robustness, efficiency, cost, size and footprint and so on. Modular multilevel converter is proposed to overcome these high end requirements. Their modular submodule structure provides many favorable features such as adaptability to broad power and voltage ranges, advanced redundancy, independence from the fast developing state-of-the-art of semiconductor devices, resilience, and ability to continue to operation in case of a fault. Moreover, their stepped output voltage waveform provides low harmonic content compared to conventional VSCs, which eliminates the usage of big, lossy, and costly output harmonic filters. They do not need a centralized energy storage element or separate dc

sources. The switching frequency of the semiconductors may even be decreased to fundamental frequency, enabling low switching loss and easing thermal management of semiconductors [12]. Also, the input current distortion and output common-mode voltage of MMC are less than conventional VSCs. In conclusion an improved voltage balancing algorithm is proposed for conventional MMC is proposed. The propose method is effectively balancing the sub-module capacitor voltages. The implementation of proposed methodology, mainly involves with programming MATLAB function. The performances under different circuit parameters or operating conditions of the adapted method are found to be satisfactory. This method improved the balancing of the capacitors voltage better than conventional PD method in each submodule. Though the THD is less than conventional PD method but there are nearly 35% THD in the system which is unwanted. In the future research the THD will be minimized.

REFERENCES

- [1] S. Debnath, J. Qin, B. Bahrani, M. Saeedifard and P. Barbosa, "Operation, Control, and Applications of the Modular Multilevel Converter: A Review," in IEEE Transactions on Power Electronics, vol. 30, no. 1, pp. 37-53, Jan 2015.
- [2] A.Nami, J. Liang, F. Dijkhuizen and G. D. Demetriades, "Modular Multilevel Converters for HVDC Applications: Review on Converter Cells and Functionalities," in IEEE Transactions on Power Electronics, vol. 30, no. 1, pp. 18-36, Jan 2015.
- [3] A. Antonopoulos, L. Angquist and H. -P. Nee, "On dynamics and voltage control of the Modular Multilevel Converter," 13th European Conference on Power Electronics and Applications, Barcelona, Spain, pp. 1-10, March 2009.
- [4] S. Rohner, S. Bernet, M. Hiller and R. Sommer, "Modelling, simulation and analysis of a Modular Multilevel Converter for medium voltage applications," IEEE International Conference on Industrial Technology, Via del Mar, Chile, pp. 775782, March 2010.
- [5] Zeng, L. Xu, L. Yao and B. W. Williams, "Design and Operation of a Hybrid Modular Multilevel Converter," in IEEE Transactions on Power Electronics, vol. 30, no. 3, pp. 1137- 1146, March 2015.
- [6] A.Lesnicar and R. Marquardt, "An innovative modular multilevel converter topology suitable for a wide power range," IEEE Bologna Power Tech Conference Proceedings., Bologna, Italy, pp. 6 pp. Vol.3, April 2003.
- [7] D. Karwatzki and A. Mertens, "Generalized Control Approach for a Class of Modular Multilevel Converter Topologies," in IEEE Transactions on Power Electronics, vol. 33, no. 4, pp. 28882900, April 2018.
- [8] D. Siemaszko, A. Antonopoulos, K. Ilves, M. Vasiladiotis, L. Ängquist and H. -P. Nee, "Evaluation of control and modulation methods for modular multilevel converters," The 2010 International Power Electronics Conference - ECCE ASIA -, Sapporo, Japan, pp. 746-753, Jan 2010.
- [9] R. Marquardt, "Modular Multilevel Converter: An universal concept for HVDC-Networks and extended DC-Bus-. applications," The 2010 International Power Electronics Conference - ECCE ASIA -, Sapporo, Japan, pp. 502-507, Jan2010 [10] E. Solas, G. Abad, J. A. Barrena, S. Aurteneatxa, A.
- [10] Cárcaar and L. Zajac, "Modular Multilevel Converter with Different Submodule Concepts—Part II: Experimental Validation and Comparison for HVDC Application," in IEEE Transactions on Industrial Electronics, vol. 60, no. 10, pp. 4536-4545, Oct.2013.
- [11] H. Fehr and A. Gensior, "Model-Based Circulating Current References for MMC Cell Voltage Ripple Reduction and Loss-Equivalent Arm Current Assessment," 2019 21st 44 European Conference on

Power Electronics and Applications (EPE '19 ECCE Europe), Genova, Italy, 2019, pp. P.1-P.9, doi: 10.23919/EPE.2019.8914767.

[12] M. Baazouzi and F. Bacha, "Impact of the MMC level number to reduce THD rate and improve the power quality transmitted to the electrical grid," 2021 IEEE 2nd International Conference on Signal, Control and Communication (SCC), Tunis, Tunisia, 2021, pp. 344-349, doi: 10.1109/SCC53769.2021.9768340.

[13] Q. Xiao et al., "Decoupled Control Scheme for THD Reduction and One Specific Harmonic Elimination in the Modular Multilevel Converter," in IEEE Transactions on Industrial Electronics, vol. 70, no. 1, pp. 99-111, Jan. 2023, doi: 10.1109/TIE.2022.3153819.

[14] Hafeez, K.; Khan, S.A.; Van den Bossche, A.; Hasan, Q.U. Circulating Current Reduction in MMC-HVDC System Using Average Model. Appl. Sci. 2019, 9, 1383.

[15] Hafeez, Kamran, Shahid A. Khan, Alex Van den Bossche, and Qadeer Ul Hasan. 2019. "Circulating Current Reduction in MMC-HVDC System Using Average Model" Applied Sciences 9, no. 7: 1383.

[16] J. Zhang and C. Zhao, "The research of sm topology with dc fault tolerance in mmchvdc," IEEE Transactions on Power Delivery, vol. 30, pp. 1561–1568, June 2015.

[17] R. Oliveira and A. Yazdani, "A modular multilevel converter with dc fault handling capability and enhanced efficiency for hvdc system applications," IEEE Transactions on Power Electronics, vol. 32, pp. 11– 22, Jan 2017.

[18] X. Yu, Y. Wei, and Q. Jiang, "Statcom operation scheme of the cdsm- mmc during a pole-to-pole dc fault," IEEE Transactions on Power Delivery, vol. 31, pp. 1150–1159, June 2016.

[19] D. Vozikis, G. Adam, D. Holliday, and S. Finney, "An improved alternate arm converter for hvdc applications," in IECON 2018 - 44th Annual Conference of the IEEE Industrial Electronics Society, pp. 3921–3925, Oct 2018.

[20] Z. Suo, G. Li, L. Xu, R. Li, W. Wang, and Y. Chi, "Hybrid modular multilevel converter based multi-terminal dc/dc converter with min- imised full-bridge submodules ratio considering dc fault isolation," IET Renewable Power Generation, vol. 10, no. 10, pp. 1587– 1596, 2016